

Application Note

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APPLICATION NOTES

This Application Note has been compiled as a support material for our customers using U-version Vacuum Fluorescent Display Modules (described as U-version Modules in the following sections). This document contains general product information; the user is recommended to refer to the most recent product specification. Materials included in this document are meant to be reference data and actual results may vary.

Applicable Model: TEG17177

1. BUS Setup

1.1 M68/i80 Selection

The U-version VFD modules support an M68 interface bus (E&R /W) making it interchangeable with LCDs. In addition, the U-version VFD can be configured to interface to i80 bus (WR/RD) via the jumpers that are located on the printed circuit board. To short circuit the jumpers, a soldering iron will be necessary and a sharp object such as a knife is required.

1.2 8-Bit/4-Bit Selection

In both the M68 and i80 interface modes, the data bus width can be selected for 8 bit or 4 bit operation. The modes are switched from 8 bit to 4 bit by sending a software command. If you have chosen the 4-bit mode, use the upper nibble (D7-D4). An example of the initialization sequence for each case will be explained in detail in the next section.

2. Initializing Procedure

The U-version module has its BUS width set at 8 bits by default. The U-version allows the user to select either 8 bits or 4 bits. 4 bits mode allows the users to take advantage of the LCD interchangeable function.

2.1 8 bit mode

(1) Wait 260m Sec. After $V_{cc} > 4.75$: Power on reset
(2) Function Set	38H (RS=0)	: 8 bit Bus Mode
(3) Function Set	38H (RS=0)	: 8 bit Bus Mode
(4) Brightness Set	00H (RS=1)	: 100% Brightness
(5) Display OFF	08H (RS=0)	: Display OFF, Cursor OFF
		: Blinking OFF
(6) Display Clear	01H (RS=0)	: Display Clear Instruction
(7) Wait 2.3m Sec		
(8) Display ON	0CH (RS=0)	: Display ON, Cursor OFF
		: Blinking OFF
(9) Entry Mode	06H (RS=0)	: Cursor Increment

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4.2 Where can I purchase the Optical Filters?

Pre packaged Optical Filter and VFD set are available at:

No Longer Available

4.3 Where can I purchase these VFD?

www.411techsystems.com

4.4 Why do 4 characters show up on the screen when only one character is entered?

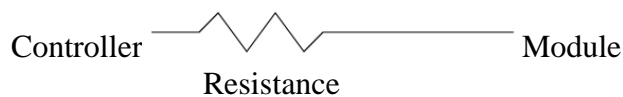
It is likely that the control signals, especially “E” contains noise caused by the length of the cable and as a result, a single write cycle is interpreted as multiple write cycles.

To prevent this from happening, observe the waveform of “E” signal with an oscilloscope for overshoot or undershoot.

Other solutions:

Method 1:

A series resistance of 100 ohms is inserted on the controller side. If the module is operated in the i80 mode, insert it for “WR”.

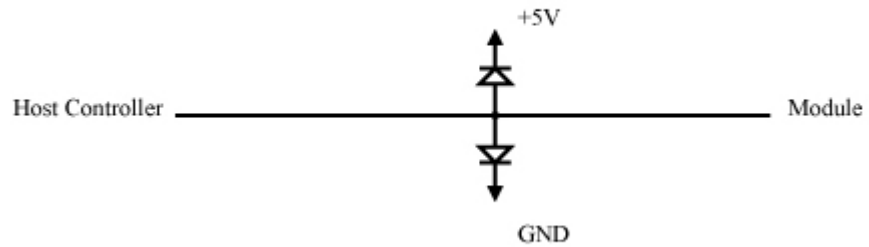


Method 2:

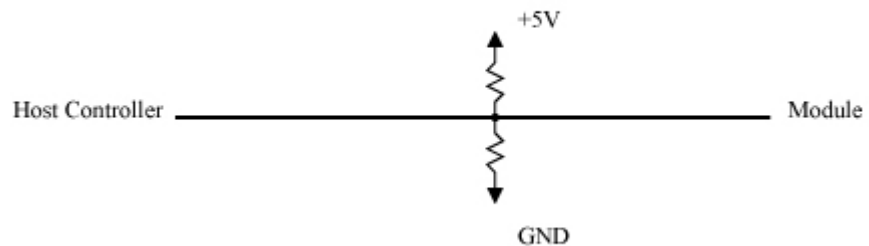
Connect the cable with a diode termination on the “E” Signal.

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A resistor with a value of 100 to 200 ohms is connected at ground. Resistor with value of 200 to 600 ohms is connected at 5V.



OR



Note: High speed controller function can distort the waveform causing the signals to be processed as multiple signals.

4.5 Comparison of In-Rush Current

The in-rush current is as follows:

Product Model	Rush Current, Peak value (A)	Sustained Period (ms)*
TEG17177	Approx. 1.5	Approx. 0.4

* Sustained time is measured from the time in-rush current starts and until it falls to half the peak value.

4.6 Sleep Mode – Reducing the Power Consumption

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Sleep mode allows the U-version modules to reduce the power consumption. Sleep mode is especially beneficial when used in a battery application.

Sleep mode is activated when Display On/Off command is sent. Sleep mode stops the DC/DC converter and cut-off filament power to the VFD, resulting in longer tube life.

Typical power consumption when the display is turned off is approximately 5mA.

4.7 Do I need to detect the display after Display OFF command?

ON/OFF is controlled by the controller not the circuit, therefore there is no need to detect the display.

4.7.1 How do you monitor the current consumption?

The current consumption depends on the state of the display, on or off and can vary from 5mA to several hundreds of mA.

5. What is the difference between TEG17177 module and LCD

	TEG17177	LCD General
BUS Interface	M68 and i80	M68 only
CG RAM SIZE	320 bit	512 bit
CGROM Font	240 characters	192 characters
No. of Lines	2 lines	1 line or 2 lines
Brightness Control	4 level brightness control function	-
Address / Data hold time	Min 700ns	Min 20ns
RS, R/W set up time	Min 20ns	Min 140ns
Data Delay From "E" and "WR"	Max 160ns	Max 320ns
Execution Time	Faster than LCD except "Display Clear"	Slower than LCD except "Display Clear"
Reset	C/R delay reset circuit. 100ms typical	No delay circuit



Safety Information

The glass portion is exposed on VFD. Caution is advised when handling VFD.

Please read the user's manual and the specifications before using the product.